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Q. 2.14: Implement the Boolean function  $F = xy + x'y' + y'z$  (a) With AND, OR, and inverter gates

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Q. 4.18: Design a combinational circuit that generates 9's and 10's complement of a BCD digit

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2710 = 110112 .315 x 2 .630 x 2 .26 x 2 .52 x 2 = = =  
= Integer 0 1 0 1 + + + + Fraction .630 .26 .52 .04 Coefficient a-1 =  
0 a-2 = 1 a-3 = 0 a-4 = 1

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$54/4 = (5*b + 4)/4 = b + 3$ , so  $5 * b = 52 - 4$ , and  $b = 8$ . (c)  $(2 * b + 4) + (b + 7) = 4b$ , so  $b = 11$ . 1.6.  $(x - 3)(x - 6) = x^2 - (6 + 3)x + 6*3 = x^2 - 11x + 22$ . Therefore:  $6 + 3 = b + 1m$ , so  $b = 8$  Also,  $6*3 = (18)10 = (22)8$ . 1.7.  $64CD 16 = 0110_0100_1100_1101$   $2 = 110_010_011_001_101 = (62315)8$ . 1.8.

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1.6.  $(x - 3)(x - 6) = x^2 - (6 + 3)x + 6*3 = x^2 - 11x + 22$  Therefore:  $6 + 3 = b + 1m$ , so  $b = 8$  Also,  $6*3 = (18)10 = (22)8$ . 1.7.  $64CD 16 = 0110_0100_1100_1101$   $2 = 110_010_011_001_101 = (62315)8$  ...

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16 51216 32 - 0  $\Rightarrow$  (200)<sub>16</sub> 2 -0  $\Downarrow$  replace each digit by binary To

binary (200)<sub>16</sub> = (10 0000 0000)<sub>2</sub> 2nd method is faster. 1.9 (a)

(11010.0101)<sub>2</sub> = 16 + 8 + 2 + 0.25 + 0.0625 = (26.3125)<sub>10</sub> (b) (A6.5)<sub>16</sub> =

10  $\times$  16 + 6 + 5  $\times$  0.0625 = (166.3125)<sub>10</sub> (c) (276.24)<sub>8</sub> = 2  $\times$  8<sup>2</sup> + 7  $\times$  8

+ 6 + 2 + 4 8 64 Digital Design With An Introduction to the Verilog HDL

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1.27 For a deck with 52 cards, we need 6 bits (diamonds, hearts, clubs, spades are encoded respectively as 00, 01, 10, a  $2^5 = 32 < 52 < 64 = 2^6$ ). Let the msnd 11. The remaining four bits b's select the suit (e.g., select the "number" of the card. Example: 0001 (ace) tqueen, king).

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Step-by-step solution: Step 1 of 7 The integer part of a decimal number is converted to any base r system by dividing the number and all successive quotients with the base value r. The remainders are accumulated after the integer quotient becomes zero and gives the value for the coefficients of r base number system.

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